

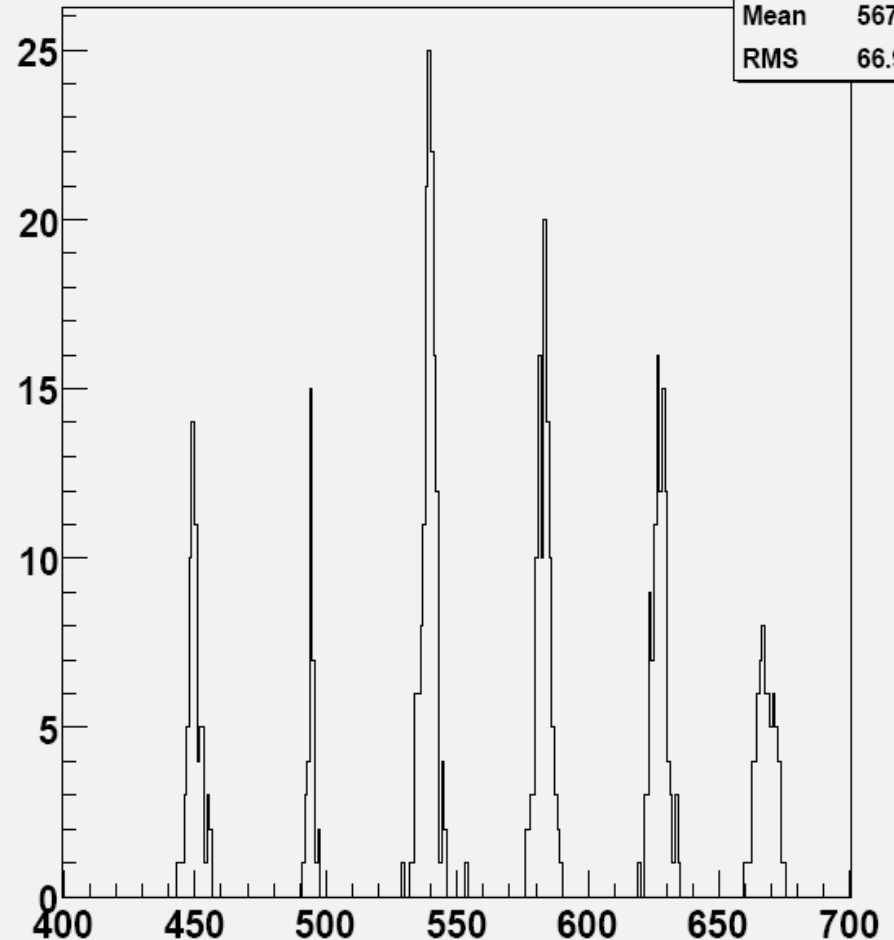
# Pixel Online Software Testing and Development Status

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# A Report

- ✓ The DAC Values, Masks and Trims now reside under *pixel/PixelConfigExamples*.
  - ✓ We are integrating Pixel FED configuration data into the same area such that PixelFEDSupervisor can load from it given a global key.
  - ❑ PixelConfigDataFormats does not currently configure the TBM. It will be done soon.
  - ❑ A column for VME Base Address will be added in the Name Translation files apart from mFEC number, mFECChannel, Hub... etc.
- ✓ FED Baseline Calibration algorithm tested. The Black level is moved to 512 by changing the Channel Offset DACs.
- ✓ FED Address Level algorithm will be tested today. Data available. Algorithm tested separately on previously collected data.

Address Level Histogram, ROC Id 0



❑ Collected data for FED Clock Phase & Delay Calibration. Phase and delay set by eye. No algorithm decided upon.

✓ Will keep everyone updated of daily details via our e-log:

<http://fpixserv.fnal.gov:8080/crl/Index.jsp>

