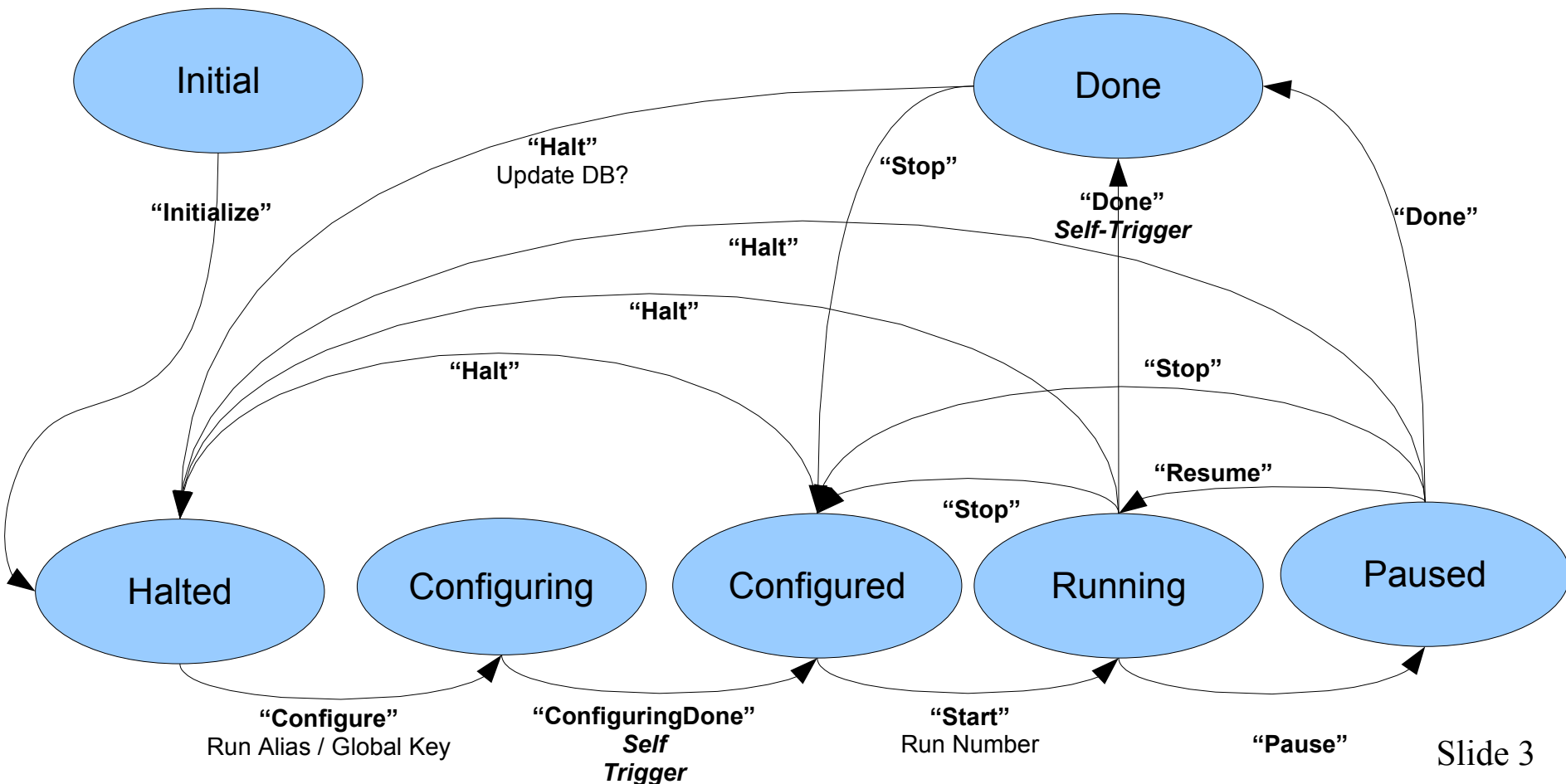# New Finite State Machine State
Configuring

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Motivations

- Parallelize the configuration of the multiple Pixel FEC and FED Supervisors.

- The Pixel DCS system, when informed by PixelFECSupervisor that it is possible to ramp up the High Voltages takes ~ 1 minute to do so. This should not hang up the PixelFECSupervisor(s) or PixelSupervisor.

- Completion % can now be reported on the GUI.

New Finite State Machine

- Common to PixelSupervisor, PixelFECSupervisor and PixelFEDSupervisor now.
- PixelSupervisor has a Synchronous FSM.
- PixelFEC/FEDSupervisors have Aysnchronous FSMs.

Structure of FSM Commands

- PixelSupervisor keeps track of the state information of underlying Supervisors. New member data SupervisorStates statePixelFECSupervisors_, statePixelFEDSupervisors introduced in PixelSupervisorConfiguration.

- Power ON Sequence should be *transparent* if there is no PixelDCSFSMInterface