

Pixel Online Software Testing and Development Status Report

- ✓ PixelConfigDataFormats configures the TBM as well as the ROCs.
- ✓ We are integrating Pixel FED configuration data into the same area such that PixelFEDSupervisor can load from it given a global key.
- ✓ FED Baseline Calibration and Address Level Calibrations tested and used regularly before Gain Calibration testing.
- ✓ Gain Calibration for a minimal set of pixels and VCal values tested.
- ❑ A column for FEC/FED Number has been added in the Name Translation files apart from mFEC number, mFECChannel, Hub. A FEC/FED Number is mapped to Crate # and VME Base Address. Working on this integration now.
- ❑ We need to run a stand-alone FECInterface test program before XDAQ can talk to the FEC. CAENVME_Init(VMEBoard, Device, Link, &aBHandle) returns -3. What does it mean?
- ❑ FED firmware gets corrupted one in a while – added a FED->loadFPGA() before FED->reset() in the Configure method of PixelFEDSupervisor.

